U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,450,553	09-1995	Kitagaki et al.	711/214
*	B	US-5,610,850	03-1997	Uratani et al.	708/671
*	C	US-5,892,966	04-1999	Petrick et al.	712/36
*	D	US-5,996,066	11-1999	Yung, Robert	712/221
*	E	US-6,028,987	02-2000	Hirairi, Koji	716/3
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Watkins et al. "A Memory Controller with an Integrated Graphics Processor". IEEE ©1993. Pages 324-338.
	V	Sun Microsystems. "Visual Instruction Set (VIS™) User's Guide". March 1997. Pages 41-43 and 87-88.
	W	Hennessy, John L. and Patterson, David A. "Computer Architecture: A Quantitative Approach." San Francisco, CA: Morgan Kaufmann Publishers, Inc., ©1996. Page 73.
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.